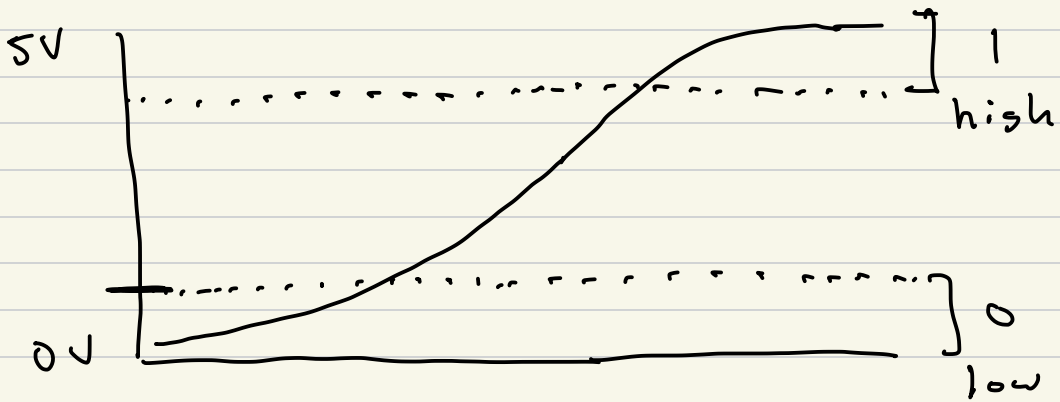


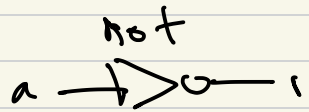
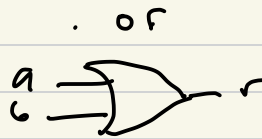
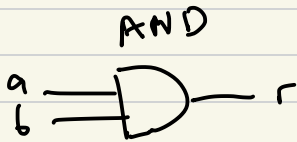
CS 315-01 Lab Intro to Digital Design

Digital Design

Analog $\rightarrow$ Digital
circuits circuits



wires & devices $\rightarrow$ gates



Code $r = a \& b$
Boolean Alg $r = a \cdot b$

$r = a \mid b$
 $r = a + b$

$r = \sim a$
 $r = \overline{a}$

Logic $r = a \wedge b$

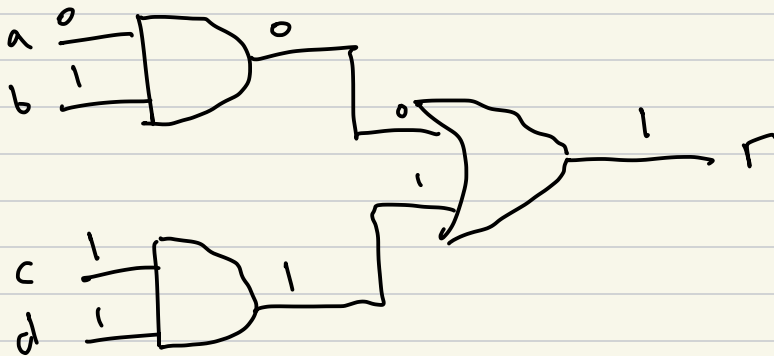
$r = a \vee b$

$r = \neg a$

a	b	r
0	0	0
0	1	0
1	0	0
1	1	1

a	b	r
0	0	0
0	1	1
1	0	1
1	1	1

a	r
0	1
1	0



$$r = (a \cdot b) + (c \cdot d)$$

Abstraction

Goal : build an 8-bit adder

